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| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449) | | | | ATTY. DOCKET NO. KIM-004 | | U.S. PATENT APPLICATION NO. 10/553,651 | |
| | | | | APPLICANT Masahiko YOSHIMOTO et al. | | | |
| | | | | FILING DATE January 17, 2006 | | GROUP 2611 | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER'S INITIALS | PATENT NO. | DATE | NAME | CLASS | SUBCLASS | FILING DATE | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
| EXAMINER'S INITIALS | PATENT NO. | DATE | COUNTRY | CLASS | SUBCLASS | Translation | |
| | | | | | | Yes | No |
| | JP H09-009251 | 01/10/1997 | Japan | | | X | |
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| OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
| | "An LSI for V_{DD} Hopping and MPEG4 System Based on the Chip" Hiroshi Kawaguchi, Gang Zhang, *Sengsoo Lee, and Takayasu Sakurai, Center for Collaborative Research, University of Tokyo 7-22-1, Roppongi, Minato-ku, Tokyo, 106-8558, JAPAN *Department of Information Electronics, Ewha University 11-1 Daehyun-dong, Seodaemun-gu, Seoul, KOREA | | | | | | |
| | A Dynamic Computation Resource Scalable Algorithm for Software Video CODEC, Fuminori OSAKO, Yoshiyuki YASHIMA, Hiroshi KOTERA, Hiroshi WATANABE, and Kazunori SHIMAMURA | | | | | | |
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| EXAMINER | | | | DATE CONSIDERED | | | |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.